## **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions and listings of claims in the application:

## **LISTING OF CLAIMS:**

- 1. (currently amended): A resin encapsulating method for a semiconductor chip comprising adhering a <u>silicone-based</u> pressure-sensitive adhesive tape to a leadframe, bonding a semiconductor chip to the leadframe having the <u>silicone-based</u> pressure-sensitive adhesive tape adhered thereto, encapsulating the semiconductor chip with a resin in a mold, and stripping the <u>silicone-based</u> pressure-sensitive adhesive tape, wherein the <u>silicone-based</u> pressure-sensitive adhesive tape has a thermal shrinkage of 3% or less on resin encapsulating and a pressure-sensitive adhesive strength of 400gf/20 mm or less at 23°C after the <u>silicone-based</u> adhesive tape being heated at 180°C.
- 2. (currently amended): A resin encapsulating method for a semiconductor chip comprising adhering a <u>silicone-based</u> pressure-sensitive adhesive tape to a tape carrier film, bonding a semiconductor chip to the tape carrier film having the <u>silicone-based</u> pressure-sensitive adhesive tape adhered thereto, encapsulating the semiconductor chip with a resin <u>andin</u> a mold, and stripping the <u>silicone-based</u> pressure-sensitive adhesive tape, wherein the <u>silicone-based</u> pressure-sensitive adhesive tape, wherein the <u>silicone-based</u> pressure-sensitive adhesive tape has a thermal shrinkage of 3% or less on resin encapsulating and a pressure-sensitive adhesive strength of 400gf/20 mm or less at 23°C after the <u>silicone-based</u> adhesive tape being heated at 180°C.

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Claims 3-5. (canceled).

6. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape has a thermal shrinkage of 2% or less on resin encapsulating.

7. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape has a thermal shrinkage of 1% or less on resin encapsulating.

- 8. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape has a pressure-sensitive adhesive strength of 300 gf/20 mm or less and 5 gf/20 mm or more.
- 9. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape further contains at least one heat-resistant filler.
- 10. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a substrate having a thickness from 5 to 250  $\mu m$ .

- 11. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a substrate having a thickness from 5 to  $100 \, \mu m$ .
- 12. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer having a thickness from 2 to  $100 \, \mu m$ .
- 13. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer having a thickness from 5 to 75 μm.
- 14. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer containing a crosslinking agent.
- 15. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a substrate subjected to primer coating or surface roughening.

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16. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer containing heat conductive particles.

17. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer containing conductive particles.

18. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape comprises a pressure-sensitive adhesive layer used in a TAB system.

19. (new): The resin encapsulating method for a semiconductor chip according to Claim 1 or Claim 2, wherein the silicone-based pressure-sensitive adhesive tape has an initial pressure-sensitive adhesive strength of about 220 gf/20 mm or less.